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**Ir Dr Bhuvendhraa Rudrusamy**



[Heriot-Watt University Malaysia](#)

I obtained a diploma in Electro-Mechanical Engineering from Politeknik Sultan Abdul Halim Mu'Adzam Shah in the year 2000. Thereafter, I worked as an Associate Product Test Engineer in Agilent Technologies and was responsible for the development and validation of Voltage Control Oscillator (VCO) devices. I then pursued my Bachelor and Master's Degree in Electrical Engineering in Universiti Teknologi Malaysia and graduated in 2004 and 2006, respectively. Subsequently, I joined Avago Technologies as an IC Design Engineer and was responsible for silicon design and validation of System on Chip camera modules and industrial encoders. I moved to Intel as Component Design Engineer in the year 2008 and was responsible for silicon verification of Intel-based System of Chip, Audio DSP and Digital Microphone ICs. During my tenure, I received many Intel awards and recognitions for delivering the product that exceeded expectations. I was Verification Architect and Validation Lead prior to pursuing my Ph.D. at Universiti Sains Malaysia in 2014. I'm currently attached to Heriot-Watt University Malaysia as Assistant Professor. My research interests include 3D shape analysis, 3D image registration, pattern recognition, computer vision, artificial intelligence, embedded systems, and Internet-of-Things. I'm a Professional Engineer with Practising Certificate, APEC Engineer, International Professional Engineer, and ASEAN Chartered Professional Engineer. I'm a Fellow of The Institution of Engineers, Malaysia, a Fellow of Higher Education Academy, an Associate Fellow of ASEAN Academy of Engineering & Technology, and Senior Member of IEEE. I was awarded with Honorary Member of ASEAN Federation of Engineering Organisation for ASEAN level contribution. Currently, I'm serving in Standing Committees of The Institution of Engineers Malaysia, Executive Committee of ASEAN Engineering Register, Executive Committee of ASEAN Federation of Engineering Organisation, Managing Editor of IEM Journal, Executive Committee of Balai Ikhtisas Malaysia, Executive Committee of IEEE Young Professionals Malaysia, Panel Evaluator of Engineering Accreditation Council Malaysia and Engineering Technology Accreditation Council Malaysia.

**IEEE Accomplishments and Activities**

(SM'22)

COMMITTEES/BOARDS: Executive Committee, IEEE YP Malaysia 2022/2023

**Statement**

Malaysia has an excellent position as an education and research hub in ASEAN, with publication output resulting in topping 100th-ranking universities worldwide. However, Organization and Society revenues and membership trends have been under pressure and have prompted numerous initiatives with limited success. By focusing on industry collaboration with the support of the local government, I believe there are many opportunities for IEEE to grow further in Malaysia, reaching out to the local community and connecting them to international IEEE experts. This promotes innovation and technology, especially in addressing challenges faced by small-medium enterprises, hence introducing product compliance to sustainable economic needs. With my broad involvement in technical communities, vast industry network, and academic experience, I can bring the experts together, collaborating in various activities that build the pillars for national growth.